

L Number	Hits	Search Text	DB	Time stamp
-	46	("6260087" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 09:39
-	22	((("6260087" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 09:49
-	34	("6260087" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 09:48
-	36	("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 10:04
-	23	((("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 09:56
-	1	((("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)) not (((("6260087" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 09:50
-	16	((("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)) and (debug\$4 analyz\$3 correct improve fix repair)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 10:04

-	16	(((((("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)) and (debug\$4 analyz\$3 correct improve fix repair)) and (select\$3 switch multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 10:04
-	0	(((((("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)) and (debug\$4 analyz\$3 correct improve fix repair)) not (((("6260087" "6173419" "6247147" "6182247" "6181159" "6178541" "6230119" "6260182" "6134173" "6182206" "6191614" "6209118" "6211697" "6219819" "6223313" "6219833" "6223148" "6226776" "6237021" "6249143" "6252422" "6253267" "6256296" "6260185").pn. and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array)) and (ASIC or application adj specific adj integrated adj circuit or standard adj cell)) and (debug\$4 analyz\$3 correct improve fix repair)) and (select\$3 switch multiplex\$3 MUX))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 10:03
-	7440	(ASIC or application adj specific adj integrated adj circuit or standard adj cell) and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array) and (debug\$4 analyz\$3 correct improve fix repair) and (select\$3 switch multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 10:05
-	877	(ASIC or application adj specific adj integrated adj circuit or standard adj cell) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array) same (debug\$4 analyz\$3 correct improve fix repair)) and (select\$3 switch multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 11:56
-	402	((ASIC or application adj specific adj integrated adj circuit or standard adj cell) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array) same (debug\$4 analyz\$3 correct improve fix repair)) and (select\$3 switch multiplex\$3 MUX)) and receiv\$3 and transmit\$4 and interfac\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 10:46
-	82	((ASIC or application adj specific adj integrated adj circuit or standard adj cell) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic adj array) same (debug\$4 analyz\$3 correct improve fix repair)) and (select\$3 switch multiplex\$3 MUX)) and receiv\$3 and transmit\$4 and interfac\$3) and (324/73.1,210 326/39,41,37,38 365/230.03,189.02,189.03,230.02 395/702,704 702/117,118 709/224 710/100,104,131 714/28,30,39,718,724,725,734,742 716/12,17).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 11:59
-	48327	(ASIC or application adj specific adj integrated adj circuit or standard near cell or component or IC or integrated adj circuit) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic) same (debug\$4 analyz\$3 compar\$3 emulat\$3)) and (select\$3 switch multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 12:00
-	2792	((ASIC or application adj specific adj integrated adj circuit or standard near cell or component or IC or integrated adj circuit) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic) same (debug\$4 analyz\$3 compar\$3 emulat\$3)) and (select\$3 switch multiplex\$3 MUX)) and (324/73.1,210 326/39,41,37,38 365/230.03,189.02,189.03,230.02 395/702,704 702/117,118 709/224 710/100,104,131 714/28,30,39,718,724,725,734,742 716/12,17).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 14:40

-	23124	(ASIC or application adj specific adj integrated adj circuit or standard adj cell or IC or integrated adj circuit) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic) same (debug\$4 analyz\$3 compar\$3 emulat\$3)) and (select\$3 switch multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 12:03
-	2252	((ASIC or application adj specific adj integrated adj circuit or standard adj cell or IC or integrated adj circuit) and ((FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic) logic) same (debug\$4 analyz\$3 compar\$3 emulat\$3)) and (select\$3 switch multiplex\$3 MUX)) and (324/73.1,210 326/39,41,37,38 365/230.03,189.02,189.03,230.02 395/702,704 702/117,118 709/224 710/100,104,131 714/28,30,39,718,724,725,734,742 716/12,17).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 12:08
-	2648	(ASIC or application adj specific adj integrated adj circuit or standard adj cell or IC or integrated adj circuit) and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic)) same (debug\$4 analyz\$3 compar\$3 emulat\$3) and (select\$3 switch multiplex\$3 MUX)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 12:07
-	625	((ASIC or application adj specific adj integrated adj circuit or standard adj cell or IC or integrated adj circuit) and (FPGA CLB PLA FPLA LAB ((programmable configurable) near2 logic)) same (debug\$4 analyz\$3 compar\$3 emulat\$3) and (select\$3 switch multiplex\$3 MUX)) and (324/73.1,210 326/39,41,37,38 365/230.03,189.02,189.03,230.02 395/702,704 702/117,118 709/224 710/100,104,131 714/28,30,39,718,724,725,734,742 716/12,17).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 12:08
-	13	(US-6577158-\$ or US-6460148-\$ or US-6260087-\$ or US-6247147-\$ or US-6173419-\$ or US-5867037-\$ or US-5794033-\$ or US-5687325-\$ or US-6178541-\$ or US-6181159-\$.did. or (US-20030062922-\$ or US-20020101260-\$.did. or (GB-2289964-\$.did.	USPAT; US-PGPUB; DERWENT	2004/04/06 14:40
-	6	((US-6577158-\$ or US-6460148-\$ or US-6260087-\$ or US-6247147-\$ or US-6173419-\$ or US-5867037-\$ or US-5794033-\$ or US-5687325-\$ or US-6178541-\$ or US-6181159-\$.did. or (US-20030062922-\$ or US-20020101260-\$.did. or (GB-2289964-\$.did.) and debug	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/06 14:42